



श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Result of B. Tech. (Electronics & Communication Engineering) Summer Semester 2025 Batch 2021 &
01 Backlog student of Batch 2019

Date: 06/202/2025

Entry No.	Student Name	Sem	CGPA	SGPA	Credits Earned	BACKLOGS
21BEC006	ABHISHEK VERMA	8.5	6.57	5.29	160	Nil
21BEC072	SHIVINDER SINGH BALIA	8.5	6.38	4.5	147	* ECL 2040 Electromagnetic Field Theory(3) * ECE 4196 Information Theory and Coding(3) * ECL 3050 Microwave Engineering(3) * ECP 2070 Digital Electronics Lab(1) * ECL 3090 Control Systems(3)
21BEC079	TAHA SAJAD	8.5	6.75	4.67	160	Nil
21BEC086	VARUN RAJPUT	8.5	6.39	5.5	157	* ECL 3050 Microwave Engineering(3)
21BEC090	VISHNU SHARMA	8.5	6.52	6	160	Nil
21BEC099	AKSHIT KAPOOR	8.5	6.83	0	160	Nil
21BEC109	ARYAVARDHAN SINGH	8.5	6.19	4.33	149	* ECC 3002 Summer Internship - II(1) * ECL 2030 Linear Integrated Circuits & Applications(3) * ECP 4203 Digital Signals Processing Lab(1) * ECL 3100 Communication & Data Networks(3) * ECL 3050 Microwave Engineering(3)
21BEC125	RAJESH KUMAR	8.5	6.47	9	160	Nil
21BEC126	RAMNIK SHARMA	8.5	7.01	5	160	Nil
21BEC129	SARTHAK SLATHIA	8.5	6.24	5.25	157	* ECL 3050 Microwave Engineering(3)
19BEC080	SARVESH	12.5	6.33	4	160	Nil

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University
but within six months of the date of declaration of this result

Sd/-

J. A / S. O (Exams)

Sd/-

AR (Exams)

Sd/-

Faculty Coordinator

Sd/-

Faculty I/c Exams