



श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Revised Result of 22BEC033 of B. Tech. (ECE) Electronics & Communication Engineering
5th & 6th Semester Batch 2022 January - May, 2025 (Even Semester)

Date: 07/10/2025

Entry No.	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
22BEC033	ASHUTOSH KUMAR	6	6.09	6.66	119	* CSL 2031 Data Structures(3) * ECL 2074 Digital System Design using Verilog(3) * ECL 3050 Microwave Engineering(3)
22BEC033	ASHUTOSH KUMAR	5	5.69	4.68	90	* CSP 2031 Data Structure Lab(1) * ECL 2071 Digital System Design using VHDL(3) * ECL 2040 Electromagnetic Field Theory(3) * ECL 2030 Linear Integrated Circuits & Applications(3) * CSL 2031 Data Structures(3)

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University
but within six months of the date of declaration of this result

Sd/-

J. A / S. O (Exams)

Sd/-

AR (Exams)

Sd/-

Faculty Coordinator

Sd/-

Faculty I/c Exams