

श्री माता वैष्णो देवी विश्वविद्यालय SHRI MATA VAISHNO DEVI UNIVERSITY

Revised Result of 22BEC033 of B. Tech. (ECE) Electronics & Communication Engineering 6^{th} Semester Batch 2022 January - May, 2025 (Even Semester)

Date: 24/09/2025

Entry No.	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
22BEC033	ASHUTOSH KUMAR	6	6.12	6.66	116	* ECL 1030 Electronic Circuits & Simulation(3) * CSL 2031 Data Structures(3) * ECL 2074 Digital System Design using Verilog(3) * ECL 3050 Microwave Engineering(3)

ERRORS & OMMISSIONS EXCEPTED

Any query or clarification may be sought from University but within six months of the date of declaration of this result

Sd/- Sd/- Sd/-

J. A / S. O (Exams) AR (Exams) Faculty Coordinator Faculty I/c Exams