



श्री माता वैष्णो देवी विश्वविद्यालय  
SHRI MATA VAISHNO DEVI UNIVERSITY

Kakryal, Katra – 182320 (J&K)

(A State University Recognized u/s 2(f) & 12(B) of UGC Act, 1956)

No. SMVDU/R&D/25/2104-2110

Dated: 31-08-2015

**NOTIFICATION**

**Subject: Constitution of DSRC of Ph.D. students of School of Computer Science & Engineering**

With respect to subject as cited above and as recommended by SRC, SoCSE, approval of the Competent Authority is hereby accorded for the constitution of DSRC of Ph.D. Student of School of Computer Science & Engineering as under:

S. No.	Name of the student & Ph.D. Entry Number	Entry Number	Proposed DSRC
1.	Ms. Subha Mahajan	23DCS001	Dr. Baijnath Kaushik, Assoc. Prof, SoCSE – Supervisor Dr. Pooja Sharma, Assoc. Prof. SoCSE – Co-Supervisor Dr. Naveen Kumar Gondhi, Assoc. Prof., SoCSE - (Expert faculty member from the School) Dr. A.K. Das, Prof., SoM- (Expert faculty member from the allied School)
2.	Mr. Tavneet Singh	23DCS002	Dr. Baijnath Kaushik, Assoc. Prof, SoCSE – Supervisor Dr. Ajay Kaul, Prof., SoCSE - (Expert faculty member from the School) Dr. A.K. Das, Prof., SoM- (Expert faculty member from the allied School)

The DSRC shall be responsible to monitor the performance of the Ph.D. student in every semester as per Rule R.10 of the Ph.D. Ordinances of SMVDU. Research Plan shall be presented by the student in front of members of DSRC and SRC together. Also, a student shall defend his/her Pre-Ph.D. Synopsis in front of members of DSRC and SRC together.

31/08/2015  
Dean (R&D)

**Copy to:**

1. Registrar, SMVDU for information.
2. Dean, FoE, for information.
3. Head, SoCSE, for information.
4. Dr. Baijnath Kaushik, Assoc. Prof., SoCSE & Ph.D. Supervisor of Student for information.
5. Concerned Faculty Members for information.
6. Faculty I/c Website for uploading on the University Website.
7. Assistant Registrar, Vice Chancellor's Secretariat for the kind information of the Hon'ble Vice Chancellor.
8. Concerned Student for information.
9. Concerned file.