



श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Result of 21BEE078 Tishant Verma of B. Tech. (EE) Electrical Engineering 7th Semester August – December, 2024 (Odd Semester) & 8th Semester January - May, 2025 (Even Semester) Batch 2021 (Earlier Withheld)

Date: 18/08/2025

Entry No.	Student Name	Sem	CGPA	SGPA	EARNED CREDITS	Backlog List
21BEE078	TISHANT VERMA	8	6.54	8	147	* ECL 2061 Microprocessor & Interfacing(3) * EEL 2311 Electrical Measurement & Instrumentation(3) * EEL 3632 Power System-II(4) * EEL 3612 Electric System Design(3)
21BEE078	TISHANT VERMA	7	6.43	7.04	137	* ECL 2061 Microprocessor & Interfacing(3) * EEL 2311 Electrical Measurement & Instrumentation(3) * EEL 3632 Power System-II(4) * EEL 3612 Electric System Design(3)

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University
but within six months of the date of declaration of this result

Sd/-

J. A / S. O (Exams)

Sd/-

AR (Exams)

Sd/-

Faculty Coordinator

Sd/-

Faculty I/c Exams