



# Shri Mata Vaishno Devi University

Kakryal, Katra-182320 (J&K) INDIA  
(A State University recognized u/s 2(f) & 12(B) of UGC)

No. : SMVDU/R&D/25/1397-1402

Dated: 19.06.2025

## OFFICE ORDER

**Subject:** Grant of financial incentive under the scheme for providing financial incentive for outstanding performance (w.r.t. Grant of patent) in favour of Dr. Deepak Sharma, Technical Assistant, School of Computer Science & Engineering.

- Ref: 1. SMVDU/ADM/ESTT./26<sup>th</sup> EC-ATR/17/3358-63 dated 6<sup>th</sup> July 2017.  
2. SMVDU/R&D/2022/33<sup>rd</sup> EC-ATR/5056-5061 dated 12<sup>th</sup> September 2022.  
3. SMVDU/R&D/25/1059-1062 dated 04.04.2025.

With reference to the above captioned subject/ references and consequent upon submission of application by Dr. Deepak Sharma, Technical Assistant, School of Computer Science & Engineering and as approved by the Competent Authority, sanction is hereby accorded for release of financial incentive of Rs. 100000/- (Rupees One Lakh only) in his favour for Grant of patent, as per details appended below:

Application No.	Title of the patent	Name of the Inventor	Applicant	Date of filing of patent	Date of publication of patent	Date of Grant of patent
201911012449	SYSTEM OF CAPACITOR VOLTAGE BALANCING OF A THREE-PHASE NEUTRAL-POINT CLAMPED BI-DIRECTIONAL RECTIFIER AND METHOD THEREOF	Dr. Deepak Sharma, Technical Assistant, School of Computer Science & Engineering	SMVDU	29.03.2019	02.10.2020	21.10.2024

This issues with the approval of the Competent Authority.

Registrar

### Copy to

1. Dean FoE / Head SoCSE for information.
2. Finance Officer, SMVDU for information and for release of incentive in favour of Dr. Deepak Sharma, TA, SoCSE.
3. AR(VC Secretariat) for kind information of the HVC.
4. I/c Website for uploading on the University website.
5. Concerned Inventor for information.
6. Concerned file / personal file of the concerned.