



## श्री माता वैष्णो देवी विश्वविद्यालय

### SHRI MATA VAISHNO DEVI UNIVERSITY

Result of 21BEC023 Ayush Jamwal of B. Tech. (ECE) Electronics & Communication Engineering  
7<sup>th</sup> Semester Batch 2021 August - December 2024 (Odd semester) (Earlier Withheld)

**Date: 16/05/2025**

Entry No.	Student Name	Sem	CGPA	SGPA	EARNED CREDITS	Backlog List
21BEC023	AYUSH JAMWAL	7	6.60	7.46	144	* ECL 2030 Linear Integrated Circuits & Applications (3) * ECL 3100 Communication & Data Networks (3)

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University  
but within six months of the date of declaration of this result

Sd/-

J. A / S. O (Exams)

Sd/-

AR (Exams)

Sd/-

Faculty Coordinator

Sd/-

Faculty I/c Exams