

Shri Mata Vaishno Devi University (Approved Under Section 2(f) and 12(B) of UGC act of 1956) School of Electronics & Communication Engineering Faculty of Engineering, SMVDU Kakryal, Katra 182320

No: SMVDU/SoECE/24/923

Date: 28.01.2025

NOTIFICATION

Open Day program on 29th January 2025 Subject:

As discussed in the meeting held on 27th Jan. 2025, it is for the information of all that the following teams are notified for coordination of Open day for students scheduled on 29th Jan. 2025:

Coordination Team during First Half

Dr. Manish Sabraj, Associate Professor Dr. Purnima Hazra, Assistant Professor Mr. Sanjeev Wadhawan, TA Mr. Pankaj Kumar, LA

Coordination Team during Second Half

Dr. Vikram Singh, Associate Professor Mr. Swastik Gupta, Assistant Professor Mr. Bhawani Singh, STA Mr. Narrotam Singh, LA

Mr. Deepankur and Mr. Munish will look after the arrangement for School Pamphlet. Other faculty and staff members are requested to conduct their classes as per schedule.

Minute-to-minute schedule of the Open Day program at SMVDU on 29th January 2025:

Time	Activity
10:00 AM - 10:20AM	Brief Introduction about the program by Dean Academics followed by address by deans of
	respective faculties.
10:20 AM - 12:00 PM	Student groups shall disperse for visit to various deputs.
12:00 PM - 1:00 PM	Lunch at Central Mess
1:00 PM - 3:00 PM	Student groups shall disperse for visit to various deptts.
3:30 PM - 4:00 PM	Groups assemble at the Auditorium for feedback.
1:00 PM	V.C's Address and departure.

*All are requested to adhere to the abc

Head SOECE 25/01/25

Copy to:

- 1. Dean FoE for information.
- 2. Fac. Incharge Website for information and uploading on University's website.
- 3. All concerned for information and necessary action to make this activity smooth.
- 4. Concerned file.