



श्री माता वैष्णो देवी विश्वविद्यालय
Shri Mata Vaishno Devi University

School of Electronics & Communication Engineering
Sub Post Office, Katra 182320 (Jammu & Kashmir)

No: SMVDU/FoE/SoECE/24/ 645

Date: 22-10-2024

Ph.D. Viva Voce Examination Notice

It is hereby notified for the information of all concerned that as approved by the Hon'ble Vice Chancellor, the open Oral Defense of the Ph.D. thesis on the title, "Design and Analysis of Multi-Valued Digital Logic Circuits" of Ms. Tabassum Khurshid (20DEC001), Ph.D. student, School of Electronics & Communication Engineering shall be conducted by the Committee on 28-10-2024 at 10:00 AM in the Administration Block Committee Room, SMVDU Campus.

All faculty members and Ph.D. students of the schools and others interested are invited to be present for the Ph.D. viva voce examination.

Ving 22/10/2024

Dr. Vikram Singh

School of Electronics & Communication Engineering
Supervisor

School Notice Board

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11. Faculty I/c Media Cell for information and n.a.
12. Ms./Mr. Tabassum Khurshid, Ph.D. student, School of ECE, for information and with a direction to present himself/herself well in time with PowerPoint Presentation for his/her defense of Ph.D. thesis.