



श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Revised Result of 21BEE078 of B. Tech. (Electrical Engineering) Summer Semester Batch 2021

Date: **10-09-2024**

Entry No.	Student Name	Sem	CGPA	SGPA	EARNED CREDITS	Backlog List
21BEE078	TISHANT VERMA	6.5	6.3	5.33	113	* MTL 2023 Integral Transforms & Complex Analysis(3) * PCL 2042 Introduction to Logic(3) * ECL 2061 Microprocessor & Interfacing(3) * EEL 2311 Electrical Measurement & Instrumentation(3) * EEL 3632 Power System-II(4) * EEL 3612 Electric System Design(3)

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University but within six months of the date of declaration of this result

Sd/-

Sd/-

Sd/-

Sd/-

J. A / S. O (Exams)

AR (Exams)

Faculty Coordinator

Faculty I/c Exams