



श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Result of B. Tech. (Electronics & Communication Engineering) Summer Semester 2024 Batch 2022

Date: **10-09-2024**

Entry No.	Student Name	Sem	CGPA	SGPA	EARNED CREDITS	Backlog List
22BEC016	ALOK RANJAN	4.5	5.81	3.33	59	* ECL 1010 Basic Electronics(3) * CSL 1028 Programming using Python(2) * ECL 2040 Electromagnetic Field Theory(3) * ECL 2070 Digital Electronics(3) * CSL 2031 Data Structures(3) * ECL 1022 Network Analysis & Synthesis(3) * ECL 2180 Signal & Systems(3)
22BEC033	ASHUTOSH KUMAR	4.5	5.88	4.25	60	* CSP 2031 Data Structure Lab(1) * ECL 2061 Microprocessor & Interfacing(3) * ECL 2040 Electromagnetic Field Theory(3) * ECL 2030 Linear Integrated Circuits & Applications(3) * ECL 1030 Electronic Circuits & Simulation(3) * CSL 2031 Data Structures(3) * ECL 2180 Signal & Systems(3)
22BEC037	AYUSHI RAJ	4.5	5.72	3.75	61	* MTL 1026 Engineering Mathematics-II(3) * ECL 2061 Microprocessor & Interfacing(3) * ECL 2040 Electromagnetic Field Theory(3) * ECL 2030 Linear Integrated Circuits & Applications(3) * ECL 1022 Network Analysis & Synthesis(3) * ECL 2180 Signal & Systems(3)
22BEC048	HARISH PATEL	4.5	6.91	4.33	67	* ECL 2040 Electromagnetic Field Theory(3) * ECL 2152 Digital Communication Engineering(3) * ECL 1022 Network Analysis & Synthesis(3) * ECL 2180 Signal & Systems(3)
22BEC068	NITIN KUNDAL	4.5	5.39	3	54	* ECL 1010 Basic Electronics(3) * PCL 2042 Introduction to Logic(3) * ECL 2040 Electromagnetic Field Theory(3) * ECL 2070 Digital Electronics(3) * ECL 2030 Linear Integrated Circuits & Applications(3) * ECL 2152 Digital Communication Engineering(3) * ECL 1030 Electronic Circuits & Simulation(3) * ECL 2180 Signal & Systems(3) * ECP 2061 Microprocessor & Interfacing Lab(1)

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University
but within six months of the date of declaration of this result

Sd/-

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J. A / S. O (Exams)

AR (Exams)

Faculty Coordinator

Faculty I/c Exams