



## श्री माता वैष्णो देवी विश्वविद्यालय

### SHRI MATA VAISHNO DEVI UNIVERSITY

Result of B. Tech. (ECE) Electronics & Communication Engineering 6<sup>th</sup> Semester Batch 2021  
January - May 2024 (Even semester) (Earlier awarded X Grade)

**Date: 13/08/2024**

Entry No.	Student Name	Sem	CGPA	SGPA	EARNED CREDITS	Backlog List
21BEC040	ISHAN AGARWAL	6	7.18	6.66	126	* MEL 1039 Engineering Graphics with CAD(2)
21BEC086	VARUN RAJPUT	6	6.31	4.21	116	* ECL 2041 Antenna & Wave Propagation(3) * ECL 2030 Linear Integrated Circuits & Applications(3) * ECL 3050 Microwave Engineering(3) * ECE 4171 Optoelectronic Devices(3)
21BEC114	DEVESH PRATAP SINGH CHAUHAN	6	7.77	7.72	128	Nil
21BEC123	PATEL KUSH RAMASRE	6	7.13	7.04	128	Nil
21BEC129	SARTHAK SLATHIA	6	6.36	3.91	92	* ECP 3081 Embedded Systems & Microcontrollers Lab(1) * ECL 2041 Antenna & Wave Propagation(3) * ECL 2040 Electromagnetic Field Theory(3) * ECL 2070 Digital Electronics(3) * ECL 2030 Linear Integrated Circuits & Applications(3) * CSL 2031 Data Structures(3) * ECC 2001 Summer Internship - I(1) * ECL 2074 Digital System Design using Verilog(3) * ECL 3050 Microwave Engineering(3) * ECP 2070 Digital Electronics Lab(1) * ECL 3081 Embedded Systems & Microcontrollers(3) * ECL 3090 Control Systems(3) * ECL 2130 Signal and Systems(3) * ECE 4171 Optoelectronic Devices(3)

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University  
but within six months of the date of declaration of this result

Sd/-

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J. A / S. O (Exams)

AR (Exams)

Faculty Coordinator

Faculty I/c Exams