

श्री माता वैष्णो देवी विश्वविद्यालय SHRI MATA VAISHNO DEVI UNIVERSITY

Directorate of Quality Assurance

Kakryal, Katra-182320, Jammu & Kashmir

SMVDU/DQA/2024/023-30

Dated: 2, May, 2024

NOTIFICATION

Subject: Organizing of Workshop on VHDL and FPGA Hands on Training at SoECE, SMVDU.

With reference to the joint proposal of Dr. Anil Kumar Bhardwaj, Head, SoECE, Dr. Vikram Singh, Assistant Professor, SoECE and Dr. Neeraj Tripathi, Assistant Professor, SoECE, duly recommended and forwarded by Dean, FoE and as approved by the Competent Authority, sanction is hereby accorded for the following:

- a) Organizing of Workshop on VHDL and FPGA Hands on Training at SoECE, SMVDU w.e.f. 4th to 5th May, 2024 in collaboration with Coreel Technologies India.
- b) Grant financial assistance of Rs.10,000/- (Rupees Ten Thousand only) for the said event from University funds, for smooth conduct of workshop.
- c) Release an advance of Rs. 10,000/- (Rupees Ten Thousand only) from the total sanctioned amount of Rs. 10,000/-.

Further, Dr. Anil Kumar Bhardwaj, Head, SoECE, Dr. Vikram Singh, Assistant Professor, SoECE and Dr. Neeraj Tripathi, Assistant Professor, SoECE are hereby appointed as Coordinator, Cocoordinator and Organizing Secretary, respectively for the said event. The Coordinator shall take feedback at the end of the event and submit a report on the proceedings (in the enclosed format) and feedback analysis to office DQA within a week after the conduct of event in hardcopy as well as in softcopy (office.dqa@smvdu.ac.in) for perusal and records.

This event shall have the unique no. SMVDU/RW/SoECE/2024/321 for future reference.

Copy to:

- Dean, FoE, SMVDU for information.
- 2. Registrar, SMVDU for information.
- 3. Finance Officer, SMVDU for information.
- 4. Head, SoECE, for information and compliance.
- 5. All Concerned ______for information and compliance.
- 6. AR to VC, for kind information of the Hon'ble Vice Chancellor.
- 7. I/c Website, for uploading on University website.
- 8. Concerned file.