



श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Result of 20BCS109 Surya V Bhamidipati of B. Tech. (CSE) Computer Science & Engineering
5th to 7th Semester Batch 2020

Date: 08/04/2024

Entry No	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
20BCS109	SURYA V BHAMIDIPATI**	7	6.45	4.29	31	* PCL 2042 Introduction to Logic(3) * CSL 2041 Theory of Computation(4) * CSP 1028 Programming Using Python Lab(2) * CSP 2051 Operating System Lab(1) * CSL 3032 Design & Algorithm Analysis(3) * CSL 4144 Nature Inspired Algorithms(3) * CSP 3032 Design & Algorithm Analysis Lab(1) * CSD 4002 Mini Project(4)
20BCS109	SURYA V BHAMIDIPATI**	6	6.47	3.7	17	* MEL 1039 Engineering Graphics with CAD(2) * PCL 2042 Introduction to Logic(3) * CSL 2041 Theory of Computation(4) * CSP 1028 Programming Using Python Lab(2) * ECL 2070 Digital Electronics(3) * CSP 2051 Operating System Lab(1) * CSL 3032 Design & Algorithm Analysis(3) * ECP 2070 Digital Electronics Lab(1) * CSP 3032 Design & Algorithm Analysis Lab(1)
20BCS109	SURYA V BHAMIDIPATI**	5	5.14	2.82	7	* CSL 2041 Theory of Computation(4) * CSP 2051 Operating System Lab(1) * MEL 1039 Engineering Graphics with CAD(2) * PCL 2042 Introduction to Logic(3)

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University
but within six months of the date of declaration of this result

** Lateral Entry

Sd/-

Sd/-

Sd/-

Sd/-

J. A / S. O (Exams)

AR (Exams)

Faculty Coordinator

Faculty I/c Exams