



श्री माता वैष्णो देवी विश्वविद्यालय
SHRI MATA VAISHNO DEVI UNIVERSITY

Kakryal, Katra – 182320 (J&K)
(A State University Recognized u/s 2(f) & 12(B) of UGC Act, 1956)

No. SMVDU/R&D/24/ 353-360

Dated: 07.02.2024

NOTIFICATION

It is hereby notified that the Ph.D. thesis of Ms. Kajal, (18DEC002), Ph.D. student, School of Electronics and Communication Engineering, Faculty of Engineering, entitled “**Designing and Simulation of Low Power PVT Variations Aware FinFET Circuits**” which was submitted under the supervision of Dr. Vijay Kumar Sharma, Assistant Professor, School of Electronics and Communication Engineering; for the award of Ph.D. degree, has been accepted on the recommendations of Ph.D. Thesis Defense Committee of the University on 01.02.2024, and she is declared eligible for the award of Ph.D. degree w.e.f. 02.02.2024.

The degree will be conferred on her in the next Convocation of the University.


Registrar

Copy to:

1. Dean, FoE for information.
2. Head, SoECE for information.
3. Finance Officer, for information.
4. Dr. Vijay Kumar Sharma, Assistant Professor, School of ECE, for information
5. Assistant Registrar, VC's Secretariat, for kind information of the Hon'ble Vice Chancellor.
6. Faculty I/c Central Library, for information & uploading the final version of thesis on Shodh Ganga website.
7. Faculty I/c Website, for uploading on University Website.
8. Concerned _____, Ph.D. student for submitting 03 hard bound copies of Ph.D. Thesis to R&D wing and 01 hard bound copy of Ph.D. thesis to Central Library alongwith a soft copy of the same within 01 month from the date of issuance of this notification. The student must ensure that Ph.D. thesis is printed as per the approved format of University and to note that the Degree shall be conferred subject to fulfillment of the above condition.
9. Concerned file/Notification file.