



# Shri Mata Vaishno Devi University

Faculty of Engineering  
School of Electrical Engineering

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No.SMVDU/FoE/SoEE/21/

Dated: 25/07/2021

## **Virtual Minutes of the 4<sup>th</sup> Board of Studies Meeting of the School of Electrical Engineering (SoEE), SMVDU**

A virtual meeting (through e-mail) of Board of Studies of the School of Electrical Engineering was held on 25<sup>th</sup> July 2021 to discuss various academic matters related to U.G. courses offered by the School of Electrical Engineering. The following members including the invited expert attended the meeting.

- |   |                 |
|---|-----------------|
| 1. Dr. Manish Sabraj, Associate Professor, Head, SoEE                                       | Chairman        |
| 2. Prof. Jagdish Kumar, Professor, Punjab Engineering College,<br>Chandigarh                | External Member |
| 3. Mr. Tushar Sud, Partner, Deloitte Touche Tohmatsu India LLP<br>(Deloitte India), Gurgaon | External Member |
| 4. Dr. Sumeet Gupta, Associate Professor  | Invited Member  |
| 5. Dr. Kamaldeep, Assistant Professor   | Member          |
| 6. Dr. Surajeet Sannigrahi, Assistant Professor   | Member          |
| 7. Mr. Shubham Chib, Assistant Professor  | Member          |



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## **Agenda Items discussed & the resolution adopted during 4<sup>th</sup> Meeting of Board of Studies of School of Electrical Engineering**

### **Agenda Item No.1: Course Structure for B. Tech. EE for 2020 onwards**

Resolution: In view of the extraordinary situation arising out of the pandemic COVID-19 and the statutory body guidelines, the course structure of B.Tech. EE is re-drafted and is finalized for the sessions 2020 onwards. The copy of the course structure is attached as **Annexure I**

The course structure of B.Tech. EE is drafted considering the Program Educational Objectives (PEO).

A broad multistep measure has been set up for the plan of program educational program to guarantee that the educational program is consistently revived and remains in a state of agreement with the speedy changes in the business prerequisites.

#### **Feedback Process**

The school has taken due consideration for taking inputs from the stakeholders

- The feedback from the students has been taken through various Mentor Mentee meeting done by the respective faculty members and is tabulated in the AAC of the school.
- The parent teacher interactions are an ongoing phenomenon where the parent is encouraged to come and see all the Laboratories, facilities and meet the faculty members to give their feedback.
- Through the Alumni network the school has taken feedback regarding the latest industry trends and need for curriculum changes.
- Feedback is obtained from the industry mentors who mentor the students in their internships/ summer training. This is very useful as the students typically work on live projects in the industry and the industry has sufficient time to observe any points of concern. This feedback is, therefore, very helpful.

#### **Curriculum Update Processes that received inputs from the above feedback mechanisms and act thereupon**

- The school has invited proposals from the faculty members for Curriculum changes and introduction of new courses.
- The BoS also considered the feedback obtained from various sources for curriculum update and restructure to meet the PEO.



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## Action Taken Report on 3<sup>rd</sup> BoS meeting held on 26<sup>th</sup> May 2020

### Agenda Item No. 1: Regarding summer Internship/Summer Training of the B.Tech. Students

*Action Taken: Needful done and notified.*

### Agenda Item No. 2: Regarding the Evaluation Mechanism

*Action Taken: Needful done and notified.*

**Dr. Kamaldeep**  
(Asst. Prof.)  
Member

**Dr. Surajeet Sannigrahi**  
(Asst. Prof.)  
Member

**Dr. Shubham Chib**  
(Asst. Prof.)  
Member

**Dr. Sumeet Gupta**  
(Assoc. Prof.)  
Invited Member

**Mr. Tushar Sud,**  
Partner, Deloitte  
External Member

**Prof. Jagdish Kumar**  
(Prof.)  
External Member

**Dr. Manish Sabraj**  
(Head, SoEE)  
Chairman

Copy to:

- PS to VC for the kind information to Hon'ble VC
- Dean FOE
- Registrar
- DAQ
- DAA
- File



# Shri Mata Vaishno Devi University

## Faculty of Engineering School of Electrical Engineering

### Annexure I

#### Program Structure of the B. Tech (Electrical Engineering) Program, 2020 – 24

##### Semester 1

Course Code	Course Title	L	T	P	C
PHL 1012	Engineering Physics	3	0	0	3
PHP 1012	Engineering Physics Lab	0	0	2	1
EEL 1006	Fundamental of Electrical Engineering	3	0	1	3
EEL 1006	Fundamental of Electrical Engineering Lab	0	0	2	1
MTL 1025	Engineering Mathematics-I	3	0	0	3
CSL 1022	Introduction to 'C' Programming	3	0	0	3
CSP 1022	'C' Programming Lab	0	0	2	1
LNL 1411	Professional Communication	2	0	0	2
LNP 1411	Professional Communication Lab	0	0	2	1
MEL 1039	Engineering Graphics with CAD	1	0	2	2
EEL 1006	Introduction to Electrical Engineering	1	0	0	NC
	Induction Program				NC
PCN 1010	NSS				NC
	<b>Total Credits</b>				<b>20</b>

##### Semester 2

Course Code	Course Title	L	T	P	C
MTL 1026	Engineering Mathematics II	3	0	0	3
MEP 1114	Workshop	0	0	2	1
CSL 1028	Programming using Python	2	0	0	2
CSP 1028	Python Programming Lab	0	0	4	2
ECL 1010	Basic Electronics	3	0	0	3
ECP 1010	Basic Electronics Lab	0	0	2	1
MEL 1012	Engineering Mechanics	3	1	0	4
EEL 1007	Electric Circuit Analysis*	3	0	0	3
PCL 1067	Discourse on Human Virtues	3	0	0	3
LNP 1142	Language Lab	0	0	2	1
	<b>Total Credits</b>				<b>23</b>



# Shri Mata Vaishno Devi University

## Faculty of Engineering School of Electrical Engineering

### Semester 3

Course Code	Course Title	L	T	P	C
PCL 2042	Introduction to Logic	3	0	0	3
MTL 2023	Integral Transforms & Complex Analysis	3	0	0	3
ECL 2070	Digital Electronics	3	0	0	3
ECP 2070	Digital Electronics Lab	0	0	2	1
EEL 2321	Electrical Machine-I	3	0	0	3
EEP 2321	Electrical Machine Lab-I	0	0	2	1
EEL 2311	Electrical Measurement and Instrumentation	3	0	0	3
EEP 2311	Measurement and Instrumentation Lab	0	0	2	1
EEP 2301	Electrical Workshop Lab	0	0	2	1
EEC 2381	Summer internship-I				1
BTL 2304	Environmental Studies	3	0	0	NC
	<b>Total Credits</b>				<b>20</b>

### Semester 4

Course Code	Course Title	L	T	P	C
ECL 2040	Electromagnetic Field Theory*	3	0	0	3
EEL 2422	Electrical Machine-II	3	0	0	3
EEP 2422	Electrical Machine Lab-II	0	0	2	1
EEL 2412	Analog Electronics	3	0	0	3
EEP 2412	Analog Electronics Lab	0	0	2	1
ECL 2060	Microprocessors & Interfacing	3	0	0	3
ECP 2060	Microprocessors & Interfacing Lab	0	0	2	1
EEL 2414	Signal & Systems	3	1	0	4
EEL 2413	Electric Materials	3	0	0	3
	<b>Total Credits</b>				<b>22</b>



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## Semester 5

Course Code	Course Title	L	T	P	C
	School Elective-I	3	0	0	3
	Open Elective-I	3	0	0	3
EEL 3511	Control System	3	1	0	4
EEL 3531	Power System-I*	3	0	0	3
EEP 3531	Power System Lab-I	0	0	2	1
EEL 3541	Power Electronics*	3	0	0	3
EEP 3541	Power Electronics Lab	0	0	2	1
CSL 2031	Data Structures	3	0	0	3
CSP 2031	Data Structures Lab	0	0	2	1
EED 3990	Minor Project –I (Hardware Based)				2
EEC 3582	Summer Internship-II				1
	<b>Total Credits</b>				<b>25</b>

## Semester 6

Course Code	Course Title	L	T	P	C
	School Elective-II	3	0	0	3
	Open Elective-II	3	0	0	3
EEL 3632	Power System-II*	3	0	0	3
EEL 3632	Power System Lab-II	0	0	2	1
EEL 3612	Electric System Design	3	0	0	3
EEL 3613	Power Plant Engineering	3	0	0	3
EEL 4711	Power Quality & FACTS	3	0	0	3
EED 3991	Minor Project –II				3
	<b>Total Credits</b>				<b>22</b>



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## Semester 7

Course Code	Course Title	L	T	P	C
EEC 4780	Summer Internship - III				2
	School Elective-III	3	0	0	3
	School Elective-IV	3	0	0	3
	Open Elective-III	3	0	0	3
BUL 4011	Entrepreneurship Management	3	0	0	3
PCN 3079	Constitution of India	1	0	0	NC
EED 4990	Minor Project-III				4
	<b>Total Credits</b>				<b>18</b>

## Semester 8

Course Code	Course Title	L	T	P	C
EED 4491 / EEC 4784	(Major Project + <b>Open Elective-IV -NC</b> ) / Internship				<b>10</b>
	<b>Total Credits</b>				<b>10</b>

**Total Credits**

**160 Credits**