School of Electronics & Communication Engineering, SMVDU

No. SMVDU/FoE/SoECE/24/895
Date: 24/01/2024

Ph.D. Viva Voce Examination Notice

It is hereby notified for the information of all concerned that as approved by the Hon'ble Vice Chancellor, the open Oral Defense of the Ph.D. thesis on the title, "Designing and Simulation of Low Power PVT Variations Aware FinFET Circuits" of Ms. Kajal, Ph.D. student, School of Electronics & Communication Engineering, shall be conducted by the Committee on 01/02/2024 at 11:30 AM in the Administration Block Committee Room, SMVDU Campus.

All faculty members and Ph.D. students of the schools and others interested are invited to be present for the Ph.D. viva voce examination.

24/01/24

Dr. Vijay Kumar Sharma
School of Electronics & Communication Engineering
Supervisor

School Notice Board

Copy to:

1. All Committee Members for information.
2. All Deans for information
3. All Heads of Schools for information & display on School Notice Board
4. Registrar for information
5. Assistant Registrar, Vice Chancellor's Secretariat for the information of the Hon'ble Vice Chancellor.
6. I/c Dean (R&D)
7. Dean (Academic Affairs)
8. Finance Officer for information and n.a.
9. Faculty I/c Network for information and n.a.
10. Faculty I/c AVP Cell for information and n.a.
11. Faculty I/c Media Cell for information and n.a.
12. Ms. Kajal, Ph.D. student, School of Electronics & Communication Engineering, for information and with a direction to present herself well in time with PowerPoint Presentation for her defense of Ph.D. thesis.

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