

श्री माता वैष्णो देवी विश्वविद्यालय SHRI MATA VAISHNO DEVI UNIVERSITY

Kakryal, Katra – 182320 (J&K)
(A State University Recognized u/s 2(f) & 12(B) of UGC Act, 1956)

No. SMVDU/R&D/23/ 5320 - 5328

Dated: 20.12.2023

NOTIFICATION

It is hereby notified that the Ph.D. thesis of Mr. Arun Bali (Entry No. 19DMT004), Ph.D. student, School of Mathematics, Faculty of Sciences, entitled "Study and Design of Adaptive Controllers for Nonstrict-Feedback Nonlinear Systems Using Neural Networks" which was submitted under the supervision of Dr. Uday Pratap Singh, Associate Professor, School of Mathematics (presently on EOL); for the award of Ph.D. degree, has been accepted on the recommendations of Ph.D. Thesis Defense Committee of the University on 15.12.2023, and he is declared eligible for the award of Ph.D. degree w.e.f. 16.12.2023.

The degree will be conferred on him in the next Convocation of the University.

Registrar

Copy to:

- 1. I/c Dean, Faculty of Sciences, for information.
- 2. I/c Head, School of Mathematics, for information.
- 3. Finance Officer, for information.
- 4. Dr. Uday Pratap Singh, Associate Professor, School of Mathematics (presently on EOL), for information.
- 5. Faculty I/c Central Library, for information & uploading the final version of thesis on Shodh Ganga website.
- 6. Faculty I/c Website, for uploading on University Website.
- 7. Assistant Registrar, Vice Chancellor's Secretariat, for kind information of the Hon'ble Vice Chancellor.
- 9. Concerned file/Notification file.