



श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Revised Result of 20BCS067 Ritesh Kumar Singh of B. Tech. (CSE) Computer Science & Engineering 6th Semester Batch 2020 Jan. - May 2023 (Even semester)

Date: 03/10/2023

Entry No	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
20BCS067	RITESH KUMAR SINGH	6	6.69	6.36	124	* CSL 3101 Artificial Intelligence(4)

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University
but within six months of the date of declaration of this result

Sd/-

J. A / S. O (Exams)

Sd/-

AR (Exams)

Sd/-

Faculty Coordinator

Sd/-

Faculty I/c Exams