

श्री माता वैष्णो देवी विश्वविद्यालय SHRI MATA VAISHNO DEVI UNIVERSITY

Result of 21MMC002 Arif Amin Zargar of M. Tech. (ECE) Electronics & Communication Engineering Summer Semester July 2023 Batch 2021

Date: 14-09-2023

Entry No	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
21MMC002	ARIF AMIN ZARGAR	4.5	8.11	8	74	* ECL 6051 Microwave Circuit Design(3) * ECL 6072 CMOS Digital Design(3)

ERRORS & OMMISSIONS EXCEPTED

Any query or clarification may be sought from University but within six months of the date of declaration of this result

Sd/- Sd/- Sd/- Sd/-