



श्री माता वैष्णोदेवी विश्वविद्यालय  
SHRI MATA VAISHNO DEVI UNIVERSITY  
Directorate of Quality Assurance

Kakryal, Katra-182320, Jammu & Kashmir

SMVDU/DQA/2021/0868-73

Dated: 23<sup>rd</sup> August, 2021

**NOTIFICATION**

**Sub: Organizing Webinar on Present and Future Prospects of Semiconductor Devices.**

With reference to the proposal from Dr. Manish Sabraj, Head SoECE and as approved by the Competent Authority, sanction is hereby accorded for the following:

- To organize one day Regional Level Webinar on Present and Future Prospects of Semiconductor Devices on 23<sup>rd</sup> October, 2021.
- The support of technical staff of Network Centre/ Internet Lab and Use of the IT facility of the University for the said event.
- The Financial assistance of Rs. 5,000/- (Rupees Five Thousand only) from University funds for the payment of Honorarium.

Further, Dr. Manish Sabraj, Head SoECE and Dr. Yogeshwar Singh Dadwhal, Assistant Professor, SoECE are hereby appointed as Coordinator and Organizing Secretary respectively for smooth conduct of the said event. The Coordinator shall take feedback at the end of the program and submit a report on the proceedings (in the enclosed format) and feedback analysis to DQA for perusal and records.

This event shall have the unique no. SMVDU/WB/SoECE/2021/167 for future reference.

  
20/08/21  
Registrar  
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Copy to:

1. Head, SoECE, for information and compliance.
2. Dr. Yogeshwar Singh Dadwhal for information and compliance.
3. All Concerned \_\_\_\_\_ for information and compliance.
4. PS to VC, for kind information of the Hon'ble Vice Chancellor.
5. I/c website, for uploading on University website.
6. Concerned file.