



## श्री माता वैष्णो देवी विश्वविद्यालय

### SHRI MATA VAISHNO DEVI UNIVERSITY

Result of 17BCS006 Agha Syed Numan Murtaza of B. Tech. (CSE) Computer Science & Engineering from 8<sup>th</sup> to 11<sup>th</sup> Semester Batch 2017 August - December 2022 (Odd semester)

**Date: 10/03/2023**

Entry No	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
17BCS006	AGHA SYED NUMAN MURTAZA	11	5.88	5	170	Nil
17BCS006	AGHA SYED NUMAN MURTAZA	9	5.9	7	166	* ECL 3061 Microprocessor & Interfacing( 4)
17BCS006	AGHA SYED NUMAN MURTAZA	8	5.85	5.96	159	* CSL 3032 Design & Algorithm Analysis( 3) * ECL 3061 Microprocessor & Interfacing( 4) * MTL 1012 Calculus & Linear Algebra( 3)

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University  
but within six months of the date of declaration of this result

Sd/-

J. A / S. O (Exams)

Sd/-

AR (Exams)

Sd/-

Faculty Coordinator

Sd/-

Faculty I/c Exams