<u>Faculty Profile</u> (For booklet and website)

Name: ANIL KUMAR BHARDWAJ

Designation: Assistant Professor

Department: School of ECE

Email ID: anil.bhardwaj[a]smvdu[dot]ac[dot]in

Contact Number and Extn.:9419903025, 2333

Qualification:

B.Tech ECE from PTU Jalandhar (2003)

M.Tech ECE from NIT Jalandhar (2009)

Ph.D ECE from Shri Mata Vaishno Devi University Katra (2021)

Experience: 18 Years

Teaching: 16 Research: 2 Administration: Total:

18

Areas of Interest / Specialization:

Circuit Design and VLSI.
Nano Device Modelling.
Hange Processing

Brief Bio-data:

- 1.Lecturer at IITT college of Engineering Himachal Pradesh (Sept 2003 to August 2005)
- 2.Lectuere at HCTM Kaithal (August 2005 to July 2007)
- 3. Research Assistant NIT Jalandhar (August 2007 to July 2009)
- 4. Project Faculty NIT Jalandhar SMDP –II Project (July 2009 July 2010)
- 5. Assistant Professor NIT Jalandhar (July 2010- December 2010)
- 6. Assistant Professor Shri Mata Vaishno Devi University (January 2011 -)

Administrative Positions:

1. Hostel Warden IITT College of Engineering (January 2004 to August 2005)



- 2. Entrepreneurship Development Cell (July 2006- July 2007)
- 3. Cultural Coordinator (May 2013- June 2015)
- 4. AVP Cell Faculty Incharge (January 2020)
- 5. Faculty Incharge Central Facilities (January 2020 to _____)

Research Profile

1. Consultancy to Alchemi Carbons under process.

Research Publications:

SCI -3 Scopus – 7

Google Scholar link:

https://scholar.google.com/citations?user=RqENn10AAAAJ&hl=en&authuser=4

Research Supervised:

S. No.	Year	Role	Research Topic	Status
1	2013	Supervisor	OTA Design and simulation	Completed
2	2014	Supervisor	CMOS Mixer	Completed
3	2015	Supervisor	Oversampled ADC	Completed
4	2017	Supervisor	Low Power Techniques	Completed
5	2018	Supervisor	OTA for MEMS	Completed
6	2019	Supervisor	Interconnects	Completed
7	2020	Supervisor	CNTFET	Completed

Award and Honours:

Gold Medalist in M.Tech in NIT Jalandhar.

Membership

Life Member ISTE