



श्री माता वैष्णो देवी विश्वविद्यालय
SHRI MATA VAISHNO DEVI UNIVERSITY

Kakryal, Katra – 182320 (J&K)
(A State University Recognized u/s 2(f) & 12(B) of UGC Act, 1956)

No. SMVDU/R&D/23/777-784

Dated: 08.03.2023

NOTIFICATION

Subject: List of candidates provisionally shortlisted for entrance exam and / or interview for admission to Ph.D. Programs in Faculty of Engineering at School of Energy Management/Mechanical Engineering/Electronics & Communication Engineering for the Even Semester (Academic Session 2022-23) at SMVDU

The following candidates have been provisionally shortlisted by SRC (School Research Committee) of Schools of Energy Management, Mechanical Engineering and Electronics & Communication Engineering at SMVDU for Entrance Exam and / or Interview **scheduled on 13th March 2023** for admission to Ph.D. programs at SMVDU for the Even Semester (Academic Session 2022 -23):

School of Energy Management:

Sl.	Name	Parentage	Admit No.	Provisionally eligible for
1.	Shubham Chib	Shivdeep Singh Chib	SOEM101	Exempted from written exam (GATE qualified)

School of Mechanical Engineering:

Sl.	Name	Parentage	Admit No.	Provisionally eligible for
1.	Amit Kumar	Tilku Ram	SOME101	Exempted from written exam (GATE qualified)

School of Electronics & Communication Engineering:-

Sl.	Name	Parentage	Admit No.	Provisionally eligible for
1.	Ekta Jolly	Rounaq Lal Jolly	SOECE101	Exempted from written exam (GATE qualified)
2.	Kavita Bhagat	Bansi Lal Bhagat	SOECE102	Exempted from written exam (NET qualified)
3.	Vanshika Sharma	Virender Sharma	SOECE103	Eligible for written exam
4.	Tania Gupta (Applied for registration on part-time basis)	Pawan Kumar Gupta	SOECE104	Eligible for written exam

The provisionally shortlisted candidates who have qualified UGC-NET (including JRF)/UGC-CSIR NET (including JRF)/SLET/GATE/teacher-fellowship holder or have passed M.Phil are exempted from appearing in entrance written test and will directly appear in the presentation/ interview while the candidates who have not qualified these exams shall have to appear in the written test as well as interview, if qualified for the same

(as per details mentioned at Sl. 3 of the general guidelines appended below). The written test and interview shall be conducted on 13th March 2023. The candidates are hereby informed that they must report for written test and/or interview / presentation to their respective Schools at SMVDU as per the schedule mentioned below:

Sl.	Particulars	Date and Time
1	Reporting time and document verification of all shortlisted candidates	13.03.2023
2	Written Test for the candidates who have not qualified UGC-NET (including JRF)/UGC-CSIR NET (including JRF)/SLET/GATE, M.Phil or are not teacher-fellowship holder.	(Final timing to be intimated by the concerned Schools)
3	Presentation / Interview of candidates who have qualified UGC-NET (including JRF)/UGC-CSIR NET (including JRF)/SLET/GATE/teacher-fellowship holder or have passed M.Phil.	
4	Presentation / Interview for the candidates who qualify the entrance written examination (for those candidates who have not qualified UGC-NET (including JRF)/UGC-CSIR NET (including JRF)/SLET/GATE/teacher-fellowship holder or M.Phil.)	

General guidelines for the candidates

1. The result of the Written Test will be displayed on the University website www.smvdu.ac.in and on the Notice Board of the Schools of Energy Management, Mechanical Engineering and Electronics & Communication Engineering on 13.03.2023 in the afternoon by the Chairman, SRC of the respective School(s).
2. The Entrance written test shall consist of Multiple Choice Objective Type Questions [MCQs] of the level of Post Graduate Degree examination in the concerned subject [50% subject related and 50% Research Methodology].
3. The written test will be qualifying in nature, with 50% marks as the qualifying criteria. The candidates who qualify the written test will be required to appear for an interview/viva-voce to be conducted by the SRC of respective School(s), where the candidates are required to discuss their research interest / area through a presentation before the SRC.
4. All provisionally shortlisted candidates must come prepared with a 10 minutes presentation (MS PPT).
5. All provisionally shortlisted candidates are required to bring all original documents and their self-attested photocopies (high school, intermediate, graduation, post-graduation, NET/SLET/GATE/M.Phil. etc.), in support of their claim as mentioned in the Ph.D. Application Form. They must carry a valid photo identification card like Adhaar / passport / government Id card / PAN card, etc.
6. Candidates in Government / Private / University services seeking admission on part time registration basis are required to submit "No Objection Certificate" obtained from their employer, at the time of written test / interview.
7. Candidates seeking admission to Ph.D. on part-time registration basis must have a minimum experience of 02 years as on date of application and must bring an "Experience Certificate" of 02 years duration to that effect at the time of written test/interview.
8. All provisionally shortlisted candidates must submit documentary proof in respect of their claim to their eligibility in all respects on the date of written test and interview, or else they shall not be allowed to appear in the written test and interview.

9. The Head of the respective Schools of Energy Management, Mechanical Engineering and Electronics & Communication Engineering shall submit the final result along with application forms and related documents (application forms with enclosures, award sheets, attendance sheet, question paper, answer key, etc) through SRC of the School and Dean of the Faculty of Sciences to R&D Section within next two working days for seeking approval of the Competent Authority.
10. For any queries, Head of the concerned Schools can be contacted on 01991-285524 Extn: 2361 or email id hod.dem@smvdu.ac.in (For School of Energy Management), Extn: 2346 or email id hod.dme@smvdu.ac.in (For School of Mechanical Engineering) and Extn: 2326 or email id hod.dece@smvdu.ac.in (For School of Electronics & Communication Engineering).


08/03/23.
Registrar

Note: No claim for TA/DA for appearing in the entrance examination / interview shall be entertained.

Copy to:

1. Dean FoE, for information.
2. Dean (R&D), for information.
3. I/c Head SoEM, for information and compliance.
4. Head SoME, for information and compliance.
5. Head SoECE, for information and compliance.
6. PS to VC for kind information of the Hon'ble Vice Chancellor.
7. I/c Website, for uploading the same on the University Website.
8. Concerned File.