



श्री माता वैष्णो देवी विश्वविद्यालय
SHRI MATA VAISHNO DEVI UNIVERSITY

Kakryal, Katra – 182320 (J&K)
(A State University Recognized u/s 2(f) & 12(B) of UGC Act, 1956)

No. SMVDU/R&D/23/2043-2052

Dated: 02.06.2023

NOTIFICATION

In continuation to notification issued vide no. SMVDU/R&D/21/951-960 dated: 18.02.2021, it is hereby notified that the Ph.D. thesis of Mr. Anil Kumar Bhardwaj (Entry No. 11PHDEEC01), Ph.D. student, School of Electronics & Communication Engineering, Faculty of Engineering, entitled "Modeling and Simulation of Carbon Nano Tube FET for Low Power Digital Design" which was submitted under the supervision of Dr. Sumeet Gupta, Associate Professor, School of Electronics & Communication Engineering and co-supervision of Dr. Balwinder Raj, Associate Professor, NITTTTR, Chandigarh; for the award of Ph.D. degree, was accepted on the recommendations of Ph.D. Thesis Defense Committee of the University on 10.02.2021, and he was declared eligible for the award of Ph.D. degree w.e.f. 11.02.2021.


02/06/23
Registrar
H. Vas

Copy to:

1. I/c Dean FoE, for information.
2. Head SoECE, for information.
3. Finance Officer, for information.
4. Dr. Sumeet Gupta, Associate Professor, School of Electronics & Communication Engineering, for information.
5. Dr. Balwinder Raj, Associate Professor, NITTTTR, Chandigarh, for information.
6. Asstt. Registrar, Vice Chancellor's Secretariat, for kind information of the Hon'ble Vice Chancellor.
7. Faculty I/c Central Library, for information.
8. Faculty I/c Website, for information.
9. Concerned _____, Ph.D. student for information.
10. Concerned file/Notification file.