



# Shri Mata Vaishno Devi University

Kakryal, Katra- 182320 (J&K)

Print Date 18/11/2018 11:09:15 PM

## School of Computer Science & Engineering

### Time Table (Major Examinations)

#### Semester 1 - M.Tech CSE

#### November - Odd Semester - 2018-19

Date / Day	Time	Subject / Title	Room No.	Student Entry No.	Invigilator
22/11/2018 Thursday	02:00 PM - 05:00 PM	Modeling and Simulation (CSL 6142)	Hall - Eco 7th Sem Classroom	,18MMS001- 18MMS018	Mr. Deo Prakash
24/11/2018 Saturday	09:30 AM - 12:30 PM	Discrete Mathematical Structure (CSL 6046)	Hall - Eco 7th Sem Classroom	,18MMS001- 18MMS018	Dr. Ananga Kumar Das
29/11/2018 Thursday	02:00 PM - 05:00 PM	Advanced Programming (CSL 6025)	Hall - Eco 7th Sem Classroom	,18MMS001- 18MMS018	Mr. Anuj Mahajan
30/11/2018 Friday	09:30 AM - 12:30 PM	Block Chain Coding (CSL 6275)	Hall - Eco 7th Sem Classroom	,18MMS001- 18MMS018	Mr. Anuj Mahajan
1/12/2018 Saturday	02:00 PM - 05:00 PM	Artificial Intelligence and fuzzy logic (CSL 6103)	Hall - Eco 7th Sem Classroom	,18MMS001- 18MMS018	Dr. Manoj Kumar Gupta



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### Time Table (Major Examinations)

#### Semester 3 - M.Tech CSE

#### November - Odd Semester - 2018-19

Date / Day	Time	Subject / Title	Room No.	Student Entry No.	Invigilator
22/11/2018 Thursday	02:00 PM - 05:00 PM	High Performance Computing (CSE 7223)	Hall - D 103	,17MMS001-17MMS018	Dr. Pabitra Kumar Jena
24/11/2018 Saturday	02:00 PM - 05:00 PM	Information and Coding Theory (CSL 7015)	Hall - D 103	,17MMS001-17MMS018	Dr. Ajay Koul
25/11/2018 Sunday	09:30 AM - 12:30 PM	Optimization Techniques (CSL 6042)	Hall - D 103	,17MMS001-17MMS018	Dr. Rakesh Kumar
30/11/2018 Friday	02:00 PM - 05:00 PM	Research Methodology (CSL 7133)	Hall - D 103	,17MMS001-17MMS018	Dr. Sandeep Bhogal