



Shri Mata Vaishno Devi University,
School of Electronics & Communication Engineering
Kakryal, Katra, J&K 182320, INDIA



First in Series of Eight Refresher Course on

“Two Weeks Refresher Course on Research Enhancement Tools and MATLAB Applications in Electronics and Computer Engineering”

(First FDP starts 16th MAR -25th MAR 2018)

INTRODUCTION:

Simulation tools are widely used nowadays in technical research. Out of many tools, most used is MATLAB. MATLAB® is a *high-performance* environment for technical computing. It integrates computation, visualization, and programming in an easy-to-use environment, where problems and solutions are expressed in familiar mathematical notation. The hands-on practice on these tools is must for researchers and academicians today.

OBJECTIVE:

The objective of course is to introduce participants to the functions many simulation tolls with emphasis on MATLAB® . MATLAB will serve the purpose of accurate and fast mathematical calculations, obtaining graphical representation of results, analysis and simulation of complex systems in Electronics, Electrical and Computer Engineering. At the same time, MIL is an industry standard for grabbing, displaying, archive, transfer and annotate images. The programs will lay emphasis on learning the advanced capabilities of MIL with hands on live practice with **IEEE format cameras**

format cameras

COURSE CONTENT:

- Research Tools in ECE
- Research methodology
- Data Interpretation
- Writing Research paper
- Project Funding Proposal Presentation
- Digital hygiene
- Introduction to MATLAB®
- Programming in MATLAB®
- Graphics in MATLAB®
- Signals Processing using MATLAB®
- Application in Image Processing
- Rigorous practice sessions and simulink
- Other Simulation tools (Spice, CADFEKO)
- Visit to various nearby Industry

COURSE DURATION: 16th -25th March 2018

WHO CAN REGISTER? Young faculty of AICTE Approved Engineering/MBA

Colleges/M.Sc./MCA/BCA/young scientists/Senior Technical Staff/Research Scholars. Prior knowledge of MATLAB® and other is not a prerequisite.

However, fundamental knowledge of Computers/ Programming is desirable.

REGISTRATION FEE: **Rs 1500.00 which may be refunded if successfully completes the course /Not Shortlisted (Attendance in all session is mandatory (except in exceptional cases limited to 90%) and submission/qualifying all assignments/Test. (working launch, workshop kit shall be provided. limited Boarding and Lodging arrangements may be arranged on request at university hostel/guest house on actual payment basis. NO TA/DA/medical/other allowance admissible for attending the same. young Faculty/researchers shall be given preference. Coordinator reserves the right to deny participation, if required. SMVDU rules regulations are final and binding.**

REGISTRATION PROCESS:

Participants can fill the Registration Form (*online submission*) available on website: www.smvdu.ac.in). The Participants are requested to choose one out of Eight programs. In case not shortlisted, one can fill form again. Bank Details in registration Form .

IMPORTANT DATES:

Listed for all Programs (8 programs in total spread over two-year period). Short listing of Participant list (will be available at university website and by email also)

INSTRUCTORS:

- 1) Faculty of the School of E&CE.
- 2) Experts from Academia and Industry

CONTACT: Any query regarding the course may be directed to **Dr. Amit Kant Pandit, Coordinator, FDP,** School of Electronics & Communication Eng., Shri Mata Vaishno Devi University, *C/o Public Relations Office, 67B/B, Satya Bhawan, Gandhi Nagar, Opp. Bahu Plaza, Jammu 180004*

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