



UGC sponsored Two-day Workshop
On
**Numerical Analysis in VLSI CAD
using MATLAB and SIMULINK**

14 - 15 January 2017

Organized By:

**Department of Electronics and Communication Engineering
Shri Mata Vaishno Devi University, Katra, Jammu & Kashmir, India 182320**



About Workshop: -

Numerical Analysis is widely used in semiconductor modelling and VLSI Designing.

In this workshop, a basic tutorial on MATLAB/Simulink will be delivered. The application of MATLAB/Simulink in solving numerical problems will be discussed, specially in the field of VLSI CAD.

This workshop will be useful specially for academicians, industry persons, research scholars and students of different fields of engineering and technology.

* There will be Hands-On training on MATLAB/SIMULINK on 13th Jan 2017. (9:30 am to 5:00 pm). **Please bring your Laptops with you.**

Registration Fees :- Nil

Registration-kit, lunch, high tea and certificate of participation will be given to each participant.

Duly filled registration form to be submitted via google form on or before 12th January 2017.

Who can participate:-

University / Institute Professionals
Researchers & Students of ECE/CSE/ME/Biotech
All those interested in MATLAB/Simulink

Some Eminent Speakers:

1. Dr. S Das Gupta, (IIT Roorkee)
2. Dr. B. K. Kaushik (IIT Roorkee)
3. Dr. R. K. Sharma (NIT Kurukshetra)
4. Dr. Manoj Dunhan (Deen Bandhu Chotu Ram Uni. Of Sci. & Tech., Sonapat)
5. Mr. Vikram Kumar (Experienced MATLAB Trainer)

Organizing Committee:

1. Dr. Rakesh Jha
2. Dr. Purnima Hazra
3. Dr. Vijay Kumar Sharma
4. Dr. Sachin Kumar Gupta
5. Mr. Swastik Gupta

Patron and Advisor

Prof. Sanjeev Jain
Hon'ble Vice Chancellor, SMVDU

Co-Patron

Sh. Ajay Khajuria
Registrar, SMVDU

Chairman

Dr. Vipin Kakkar, *DoECE, SMVDU*

Workshop Coordinator

Mr. Vikram Singh Mr. Neeraj Tripathi
DoECE, SMVDU

For any queries please contact:

Mr. Vikram Singh, Mr. Neeraj Tripathi
Contact No: - 9466754797, 9622332957
Email: - cad.vlsi@smvdu.ac.in

Venue: Department Of Electronics and Communication Engineering
Shri Mata Vaishno Devi University, Katra, Jammu & Kashmir, India 182320