



**Shri Mata Vaishno Devi University**  
**Consolidated Result of B.Tech.(CSE) – 1<sup>st</sup> Year , Summer Semester, June-July, 2016**  
**Dated: 01/09/2016**

S.NO	ENTRY NO	NAME	CGPA	SGPA	EARNED CREDITS	BACKLOGS
1	15BCS003	ANEESH GUPTA	5.45	4.00	33	* ECL 1020 Basic Electrical Engineering( 3) * MEL 1039 Engineering Graphics with CAD( 2) * MTL 1024 Discrete Structures( 4)
2	15BCS004	ASHISH KUMAR				RW
3	15BCS016	MANSI SHARMA	6.43	5.00	42	NIL
4	15BCS017	MANSI SHARMA	5.65	5.00	37	* MEL 1039 Engineering Graphics with CAD( 2) * MTL 1022 Differential Equations & Vector Calculus( 3)
5	15BCS034	RITHIK KUMAR NAYYAR	5.45	5.00	31	* CSL 1020 Programming with C( 4) * ECL 1020 Basic Electrical Engineering( 3) * MTL 1024 Discrete Structures( 4) * PCN 1010 NSS( 0)

\*\* Result Withheld

At the completion of the first two semesters attended by a student (excluding Semester Withdrawal), the student is required to earn 60% of the total credits as per Course structure, failing which his/her admission in the program will stand terminated and his/her name struck from the roll of the program. However, if the same student has earned 80% of the Credits (as per Course structure) in either of the first two semesters attended by him/her then his/her admission will not be terminated and he/she would be allowed to register for the 3<sup>rd</sup> Semester.

Prepared by

Sd/-

JA/OA (A&R)

Checked by

Sd/-

SO (A&R)

Verified by

Sd/-

AR (A&R)

Sd/-

I/c Associate Dean (AA)