Shri Mata Vaishno Devi University

Consolidated Result of B.Tech (ECE) 2nd Year, Summer Sem, June-July,2016
Dated: 01/09/2016

ENTRY_NO	NAME	CGPA	SGPA	EARNED CREDITS	BACKLOG
14BEC014	ANKIT KUMAR	6.46	7.00	95	NIL
14BEC031	NETINTI SUBHASH	6.42	7.00	91	NIL
14BEC012	AMAN KHAJURIA	6.32	6.00	93	* ECP 2071 VHDL Lab(1) * ECP 2152 Digital Communication Engineering Lab(1)
14BEC024	GURMANSHAAN SINGH	6.30	7.18	91	NIL
14BEC011	AKUL GUPTA	6.08	5.09	91	* EECL 402B Microwave Engineering(4)
14BEC055	SUJEET KUMAR PATHAK	6.01	7.43	91	NIL
14BEC007	AKARSHAN MAGOTRA	5.88	6.55	95	NIL
14BEC013	ANKIT KUMAR	5.86	5.45	91	NIL
14BEC002	ABHIJEET KUMAR	5.75	7.29	87	* ECL 1030 Electronic Circuits & Simulation(4)
14BEC060	VIVEK KUMAR	5.65	6.18	91	NIL
14BEC049	SAHIL PANDITA	5.64	5.00	90	* ECP 2071 VHDL Lab(1) * EECL 402B Microwave Engineering(4)
14BEC050	SAURABH PANDEY	5.58	6.18	60	Having more than 08 backlogs. For details contact Academics Wing
14BEC021	CHITIKALA VIJAY	5.39	5.33	80	* ECL 1030 Electronic Circuits & Simulation(4) * ECL 2030 Linear Integrated Circuits & Applications(4) * ECL 2151 Analog Communication Engineering(4) * PCL 2042 Introduction to Logic(3)
14BEC059	VIVEK KUMAR	5.30	3.91	76	* ECL 1030 Electronic Circuits & Simulation(4) * ECL 2040 Electromagnetic Field Theory(4) * ECL 2151 Analog Communication Engineering(4) * PCL 2042 Introduction to Logic(3)

Prepared by	Checked by	Verified by	
Sd/-	**Sd/-**	**Sd/-**	**Sd/-**
JA/OA (A&R)	**SO (A&R)**	**AR (A&R)**	**I/c Associate Dean (AA)**

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14BEC023	GAJJALA SREEKANTHREDDY	5.24	6.27	84	* CSL 1021 Introdcution to C Programming(3) * ECL 2030 Linear Integrated Circuits & Applications(4)
14BEC005	ABHISHEK KUMAR	5.09	1.45	54	Having more than 08 backlogs. For details contact Academics Wing
14BEC022	DEEPAK KUMAR KASYAP	5.07	4.00	55	Having more than 08 backlogs. For details contact Academics Wing
14BEC035	NISHANT KUMAR	4.88	4.33	67	Having more than 08 backlogs. For details contact Academics Wing

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JA/OA (A&R)	**SO (A&R)**	**AR (A&R)**	**I/c Associate Dean (AA)**