



श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Kakryal, Katra -182320, J&K (INDIA)

Ref No: SMVDU/AA/17/ 538
Dated: 28/07/2017

Notification

Subject: Candidates Provisionally selected for admission in Ph.D. program in Computer Science & Engineering at SMVD University.

The following candidates are Provisionally Selected for admission to Ph.D. program in Computer Science & Engineering offered by Department of Computer Science & Engineering, SMVDU.

S No.	Name of Candidate	Eligible for University Assistantship	Status of Registration (Full-Time/Part-Time)	School/Deptt.	Name of Allotted Supervisor & Co-Supervisor
1.	Ravdeep Kour	No	Full-Time	Computer Science & Engineering	Dr. Sakshi Arora, AP, DoCSE (Supervisor)
2.	Vinod Kumar	Yes	Full-Time	Computer Science & Engineering	Prof. Sanjeev Jain, Vice Chancellor, SMVDU (Supervisor)
3.	Kanica Sachdev	Yes	Full-Time	Computer Science & Engineering	Dr. Manoj Kumar Gupta, Associate Prof. DoCSE (Supervisor)
4.	Sakshi	Yes	Full-Time	Computer Science & Engineering	Prof. Sanjeev Jain, Vice Chancellor, SMVDU (Supervisor)
5.	Sudesh Kumar	No	Part-Time	Computer Science & Engineering	Dr. Sunanda Gupta, AP, DoCSE (Supervisor)
6.	Arjun Puri	Yes	Full-Time	Computer Science & Engineering	Dr. Manoj Kumar Gupta, Associate Prof. DoCSE (Supervisor)
7.	Abhineet Gupta	Yes	Full-Time	Computer Science & Engineering	Dr. Baijnath Kaushik, Associate Prof. DoCSE (Supervisor)
8.	Reya Sharma	Yes	Full-Time	Computer Science & Engineering	Dr. Baijnath Kaushik, Associate Prof. DoCSE (Supervisor)
9.	Insha Majeed	No	Full-Time	Computer Science & Engineering	Dr. Sakshi Arora, AP, DoCSE (Supervisor)
10.	Surbhi Sharma	No	Full-Time	Computer Science & Engineering	Dr. Baijnath Kaushik, Associate Prof. DoCSE (Supervisor)

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