



श्री माता वैष्णो देवी विश्वविद्यालय

Shri Mata Vaishno Devi University
Department of Computer Science & Engineering
Faculty of Engineering

Sub Post office-182320, Jammu & Kashmir
(Recognized under Section 12 (B) & 2 (f) of UGC Act, 1956)

Ref. No.: SMVDU/DoCSE/17/242

Dated: 16/06/2017

NOTIFICATION

This is for information of all concerned that the result for MCA Admission (2017-18) for all the selected candidates has been intimated through e-mail and the offer letters also being sent through Speed-Post

Sd/-
Chairman
Admission Committee, MCA(2017-18)